

# William J. Snow

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**SUMMARY** I am a computer engineer with experience in digital hardware design, embedded systems, and software development. I currently work on low latency futures contract trading algorithms. I specialize in low level networking and critical path optimization.

**EDUCATION** **B.S., Computer Engineering** May 2014  
Purdue University, West Lafayette, IN

**EXPERIENCE** **Wiklund Trading, LLC** Feb 2017-present  
*Hardware and Software Developer*

- Develop custom trading platform deployed on CME Globex futures and options exchange
- Spearheaded strategy for and designed firm's first FPGA deployments
- Program in C++ for the trading applications, SystemVerilog for the FPGA RTL and testbenches, Python and Java for tooling and analysis
- Knowledgeable about FIX, MDP3/SBE, TCP/IP, and Ethernet protocols, CME Globex datacenter and networking topologies
- Improved in-house network and test harness for performance measurements and continuous testing/delivery pipeline

**Chance the Rapper** Sep 2016  
*Lead Engineer*

- Engineered electronic light-up costumes for Chance the Rapper's *Magnificent Coloring Tour*
- Designed power and digital circuitry and wiring for hundreds of full color RGB LEDs, hand sewn into jacket linings
- Developed and designed lighting software in C++ using Arduino toolchain

**Jeco Plastic Products** June-Aug 2016  
*Consultant*

- Designed wireless temperature monitoring system controlled by web app interface
- Programmed in C for thermocouple driver and unix socket API, Javascript for node.js web app

**Pricesourcing.com** July-Oct 2013  
*Lead Developer*

- Prototyped a front and back end for vendor-to-customer online aggregator
- Collaborated with a professional graphic web designer for UX and SEO
- Implemented scalable back end in PHP and MySQL

## SKILLS

### Languages

C, C++, Verilog, VHDL, Assembler (x86, MIPS, Motorola 68HC11), tcl, bash, Java, Python, PHP, Javascript, Go, Ruby

### Commercial Software

Mentor Graphics (ModelSim, HDL Designer), Cadence (SOC Encounter, Virtuoso, OrCad Capture/PSpice A/D), Synopsys DC Ultra, Altera Quartus, EagleCAD, Altium Designer, TI Code Composer Studio, Arduino IDE, Matlab, CATIA V5